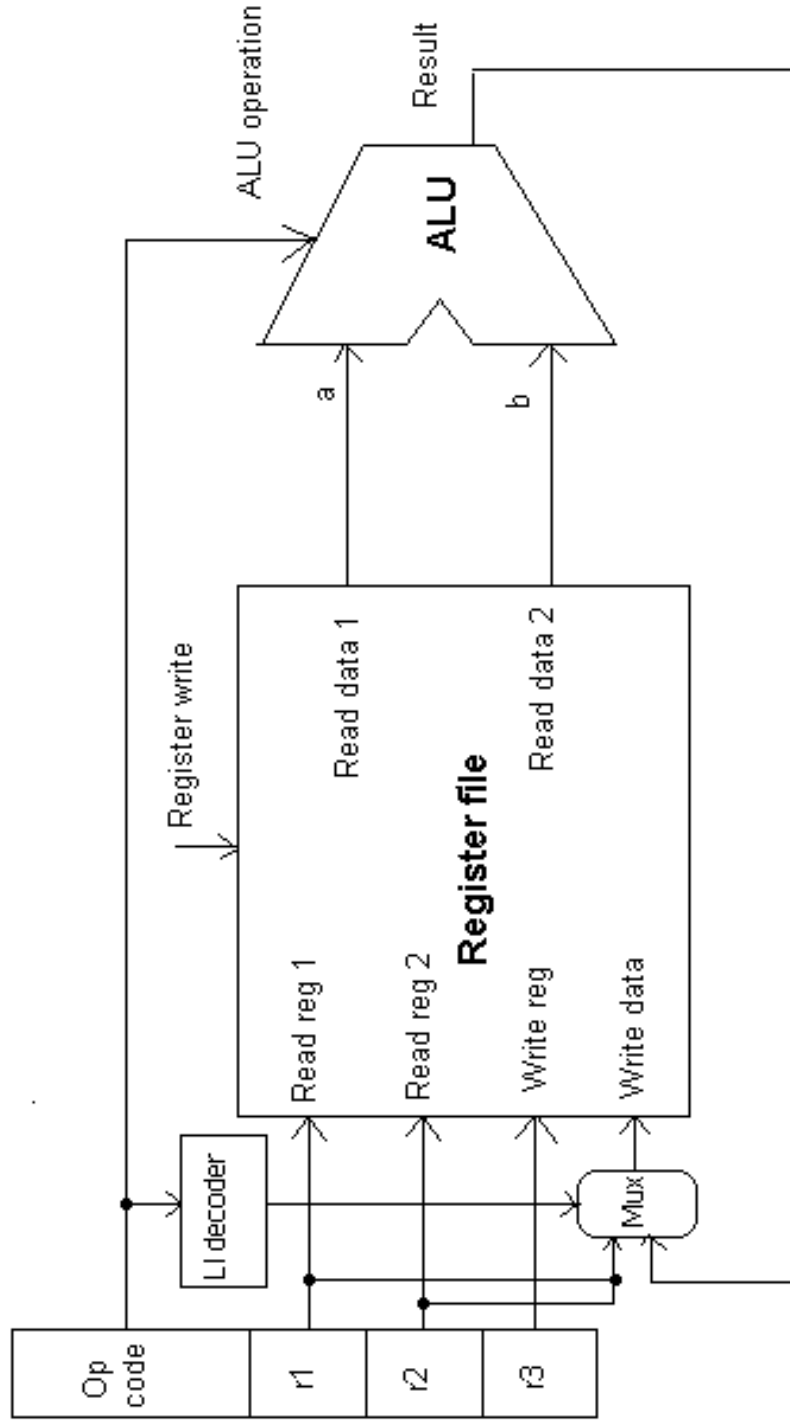


# Simple CPU

## Instruction register



R-type (AND, OR, ADD, SUB, SLT)

8	6	5	4	3	2	1	0
Op code	r1	r2	r3				

I-type (LI)

8	6	5	2	1	0
Op code	Immediate value			r3	

Instruction      OP code

AND	000
OR	001
ADD	010
SUB	110
SLT	111
LI	100